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		Application Number	10/664,722		
TRANSMITTAL		Filing Date	Sep 17, 2	Sep 17, 2003	
FORM		First Named Inventor	Bian, Jinru		
(to be used for all correspondence after Initial filing)		Art Unit	2811		
		Examiner Name	D. W. Owens		
Total Number of Pages in 3	This Submission 2	Attorney Docket Number	03019US		
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Fee Attached		Licensing-related Papers		Appe	eal Communication to Board opeals and Interferences
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Art Unit:

Examiner:

2811

D. W. Owens

OCT 2 1 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Bian et al.

Application No.: 10/664,722

Filed: 9/17/2003

Title: POLISHING COMPOSITION FOR

SEMICONDUCTOR WAFERS

Attorney Docket No.: 03019US

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Claim Election

Dear Sir:

In response to the Restriction Requirement of September 24, 2004, please elect claims Group I or composition claims 1 to 7 without traverse.

Please call Applicants' Attorney at 302-283-2136 if a call would expedite further handling of this applications.

Respectfully submitted,

Date

Attorney for Applicants

Reg. No. 34,124

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